



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of:

Hiroyoshi TOMITA et al

Patent No.: 6,320,819 B2

Issued: November 20, 2001

For: SEMICONDUCTOR DEVICE RECONCILING DIFFERENT
TIMING SIGNALS

Certificate
APR 12 2004
of Correction

SUBMISSION OF TERMINAL DISCLAIMER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


April 7, 2004

Sir:

Submitted herewith for filing in the above-identified application, is a Terminal Disclaimer Under 37 C.F.R. 1.321(a).

Attached is a check in the amount of One Hundred Ten Dollars (\$110.00) for payment of the required fee. In the event that the attached check is found to be insufficient, or if any additional fees are due with respect to this paper, please charge our Deposit Account No. 01-2300, referencing Atty. Docket No. 100353-00029.

Respectfully submitted,


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Atty. Docket No.: 100353-00029

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Enclosures: Check #402558; Terminal Disclaimer Under 37 C.F.R. 1.321(a)